

## **Amendments to the Specification**

Please **replace** the title on page 1 with the following:

-- APPARATUS AND METHOD FOR PROTECTING A MEMORY  
SHARING SIGNAL CONTROL LINES WITH OTHER CIRCUITRY --

Please **add** the following section heading before the title on page 1:

-- TITLE OF THE INVENTION --

Please **add** the following section heading after the title on page 1:

-- BACKGROUND OF THE INVENTION

(1) Field of the Invention --

Please **replace** the blank line at page 1, line 7 with the following heading:

-- (2) Description of Related Art --

Please **replace** the blank line at page 2, line 17 with the following heading:

-- BRIEF SUMMARY OF THE INVENTION --

Please **replace** the blank line at page 3, line 14 with the following heading:

-- BRIEF DESCRIPTION OF THE SEVERAL VIEWS OF THE  
DRAWINGS --

Please **replace** the blank line at page 3, line 26 with the following heading:

-- DETAILED DESCRIPTION OF THE INVENTION --

Please **replace** the paragraph beginning at page 9, line 17 with the following **rewritten** paragraph:

-- In the exemplary embodiment, EEPROM 12 includes means for preventing EEPROM 12 from loading down control lines 13 and 15 when EEPROM 12 is in the unpowered state. Generally, ICs include electrostatic discharge ("ESD") protection diodes coupled to the pins. In the present embodiment, EEPROM 12 includes the above-mentioned preventing means, for example, a zener diode D1, coupled to, for example, the Vcc pin. Various devices and methods that are

known for providing such functionality, for example, zener diodes, and bipolar transistors, may be used.--